

## Refine Search

### Search Results -

Terms	Documents
L3 and L4	14

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:





### Search History

DATE: Thursday, December 16, 2004    [Printable Copy](#)    [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=USPT; PLUR=NO; OP=ADJ</i>			
<u>L5</u>	l3 and L4	14	<u>L5</u>
<u>L4</u>	(shar\$5 or common) near1 memory	13592	<u>L4</u>
<u>L3</u>	l1 and L2	54	<u>L3</u>
<u>L2</u>	single adj address adj bus	153	<u>L2</u>
<u>L1</u>	processors or (multi-processors) or (multi adj processors) or (multiple adj processors)	80455	<u>L1</u>



END OF SEARCH HISTORY

## Freeform Search

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<b>Database:</b>	US Pre-Grant Publication Full-Text Database
	US Patents Full-Text Database
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

<b>Term:</b>	15 and L6	 
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<b>Display:</b>	<input type="text" value="15"/> Documents in <b>Display Format:</b> <input type="text" value="CIT,AB"/> Starting with Number <input type="text" value="1"/>
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**Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

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Search

Clear

Interrupt

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### Search History

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**DATE:** Thursday, December 16, 2004    [Printable Copy](#)    [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=USPT; PLUR=NO; OP=ADJ</i>		
<u>L7</u>	15 and L6	3	<u>L7</u>
<u>L6</u>	(6272604 or 5900015 or 5490253).pn.	3	<u>L6</u>
<u>L5</u>	13 and L4	14	<u>L5</u>
<u>L4</u>	(shar\$5 or common) near1 memory	13592	<u>L4</u>
<u>L3</u>	11 and L2	54	<u>L3</u>
<u>L2</u>	single adj address adj bus	153	<u>L2</u>
<u>L1</u>	processors or (multi-processors) or (multi adj processors) or (multiple adj processors)	80455	<u>L1</u>

END OF SEARCH HISTORY

## Refine Search

### Search Results -

Terms	Documents
L2 and L20	17

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L21







### Search History

 DATE: Thursday, December 16, 2004    [Printable Copy](#)    [Create Case](#)
**Set Name Query**  
 side by side

**Hit Count Set Name**  
 result set

*DB=USPT; PLUR=NO; OP=ADJ*

<u>L21</u>	l2 and L20	17	<u>L21</u>
<u>L20</u>	l17 or l18 or L19	120	<u>L20</u>
<u>L19</u>	proxy near1 node	78	<u>L19</u>
<u>L18</u>	proxy near1 nodes	28	<u>L18</u>
<u>L17</u>	l15 or L16	30	<u>L17</u>
<u>L16</u>	proxy near1 processor	29	<u>L16</u>
<u>L15</u>	proxy near1 processors	1	<u>L15</u>
<u>L14</u>	l2 and L13	1	<u>L14</u>
<u>L13</u>	l1 and L12	71	<u>L13</u>
<u>L12</u>	single near1 address near1 bus	166	<u>L12</u>
<u>L11</u>	l9 and L10	9	<u>L11</u>
<u>L10</u>	processors	80455	<u>L10</u>
<u>L9</u>	l6 with through	15	<u>L9</u>
<u>L8</u>	l6 and L7	2	<u>L8</u>

<u>L7</u>	processors with l4	448	<u>L7</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L6</u>	access\$4 with l2 with (address near1 bus)	88	<u>L6</u>
<u>L5</u>	l3 same L4	5	<u>L5</u>
<u>L4</u>	single near2 bus	8698	<u>L4</u>
<u>L3</u>	l1 same L2	268	<u>L3</u>
<u>L2</u>	(common or shar\$5) near1 memory	13922	<u>L2</u>
<u>L1</u>	master same slave	19650	<u>L1</u>

END OF SEARCH HISTORY

## Refine Search

### Search Results -

Terms	Documents
L26 and L29	0

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L30








### Search History

DATE: Thursday, December 16, 2004    [Printable Copy](#)    [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>			
<u>L30</u>	l26 and L29	0	<u>L30</u>
<u>L29</u>	single near1 address near1 bus	213	<u>L29</u>
<u>L28</u>	l26 and l12	0	<u>L28</u>
<u>L27</u>	behalf and L26	1	<u>L27</u>
<u>L26</u>	l2 same L25	50	<u>L26</u>
<u>L25</u>	plurality near3 cpus	1254	<u>L25</u>
<u>L24</u>	behalf\$2 with L23	12	<u>L24</u>
<u>L23</u>	proxy near1 (node or processor)	318	<u>L23</u>
<i>DB=USPT; PLUR=NO; OP=ADJ</i>			
<u>L22</u>	l2 and l17	3	<u>L22</u>
<u>L21</u>	l2 and L20	17	<u>L21</u>
<u>L20</u>	l17 or l18 or L19	120	<u>L20</u>
<u>L19</u>	proxy near1 node	78	<u>L19</u>
<u>L18</u>	proxy near1 nodes	28	<u>L18</u>

<u>L17</u>	l15 or L16	30	<u>L17</u>
<u>L16</u>	proxy near1 processor	29	<u>L16</u>
<u>L15</u>	proxy near1 processors	1	<u>L15</u>
<u>L14</u>	l2 and L13	1	<u>L14</u>
<u>L13</u>	l1 and L12	71	<u>L13</u>
<u>L12</u>	single near1 address near1 bus	166	<u>L12</u>
<u>L11</u>	l9 and L10	9	<u>L11</u>
<u>L10</u>	processors	80455	<u>L10</u>
<u>L9</u>	l6 with through	15	<u>L9</u>
<u>L8</u>	l6 and L7	2	<u>L8</u>
<u>L7</u>	processors with l4	448	<u>L7</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L6</u>	access\$4 with l2 with (address near1 bus)	88	<u>L6</u>
<u>L5</u>	l3 same L4	5	<u>L5</u>
<u>L4</u>	single near2 bus	8698	<u>L4</u>
<u>L3</u>	l1 same L2	268	<u>L3</u>
<u>L2</u>	(common or shar\$5) near1 memory	13922	<u>L2</u>
<u>L1</u>	master same slave	19650	<u>L1</u>

END OF SEARCH HISTORY